

FIG. 1 (PRIOR ART)

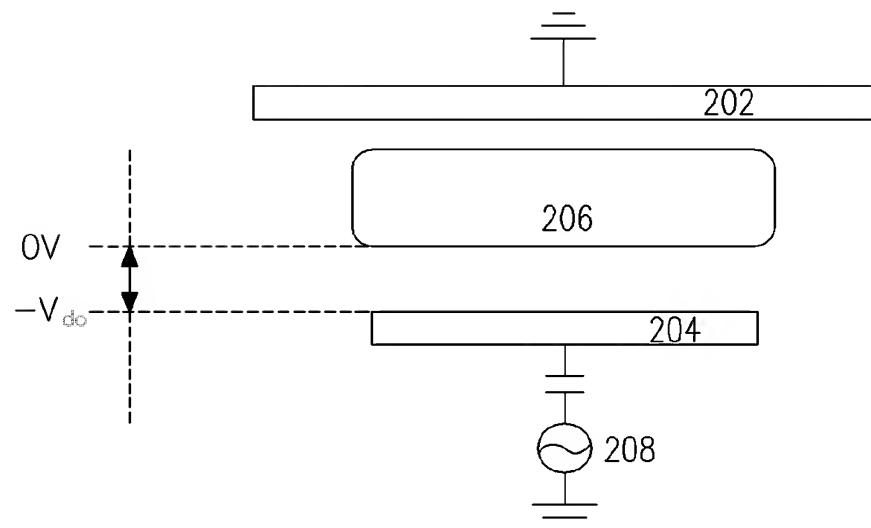


FIG. 2A

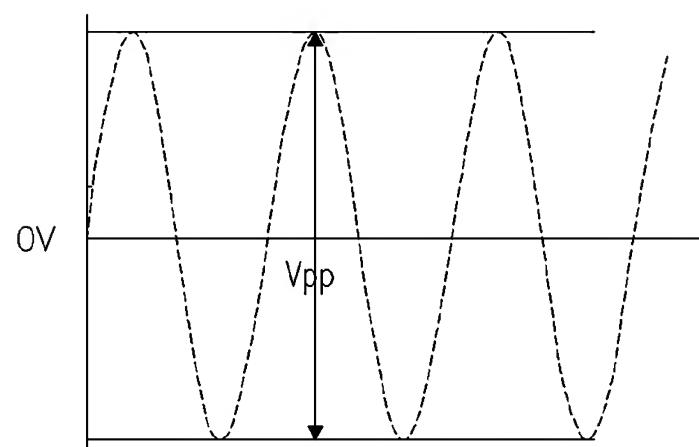
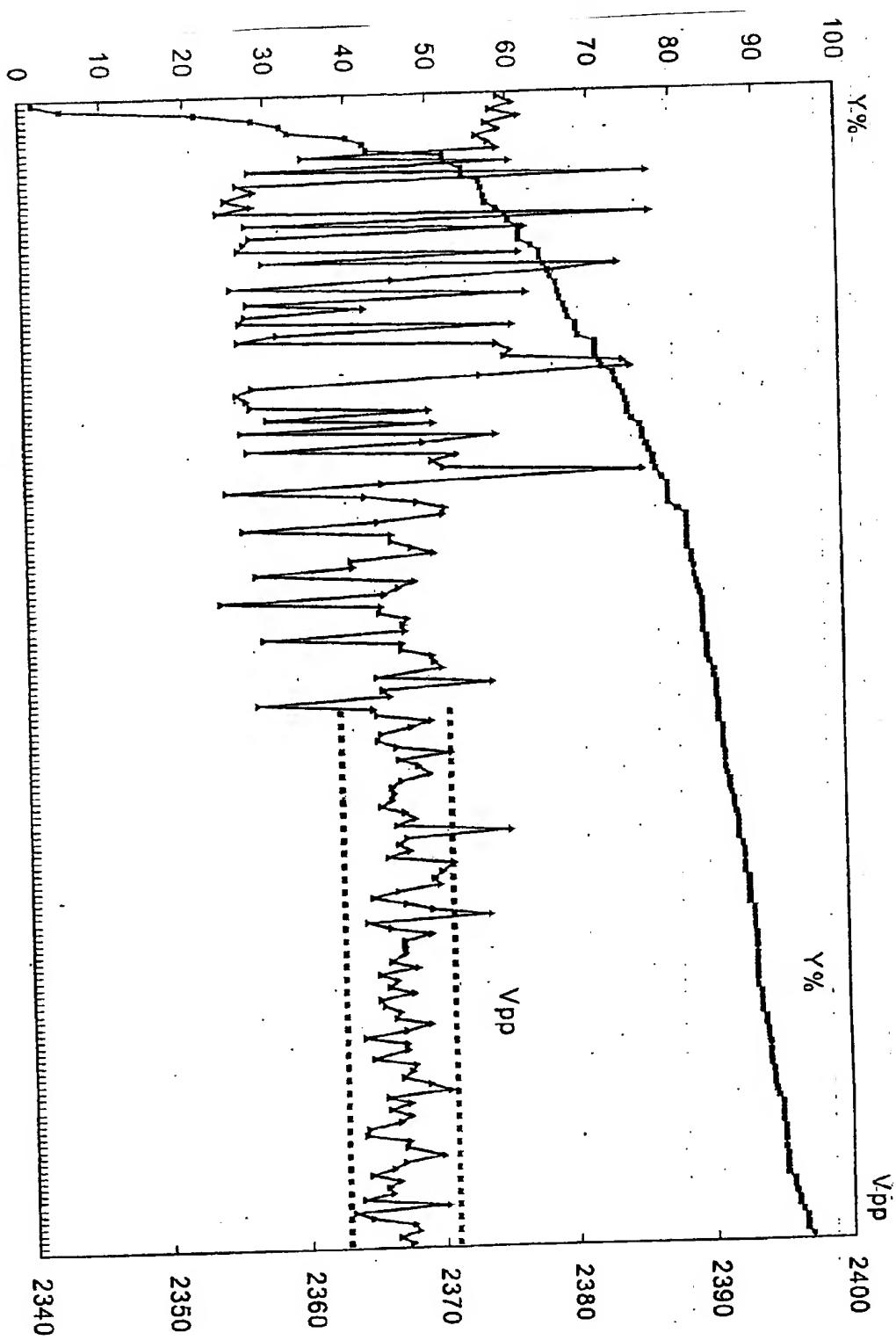
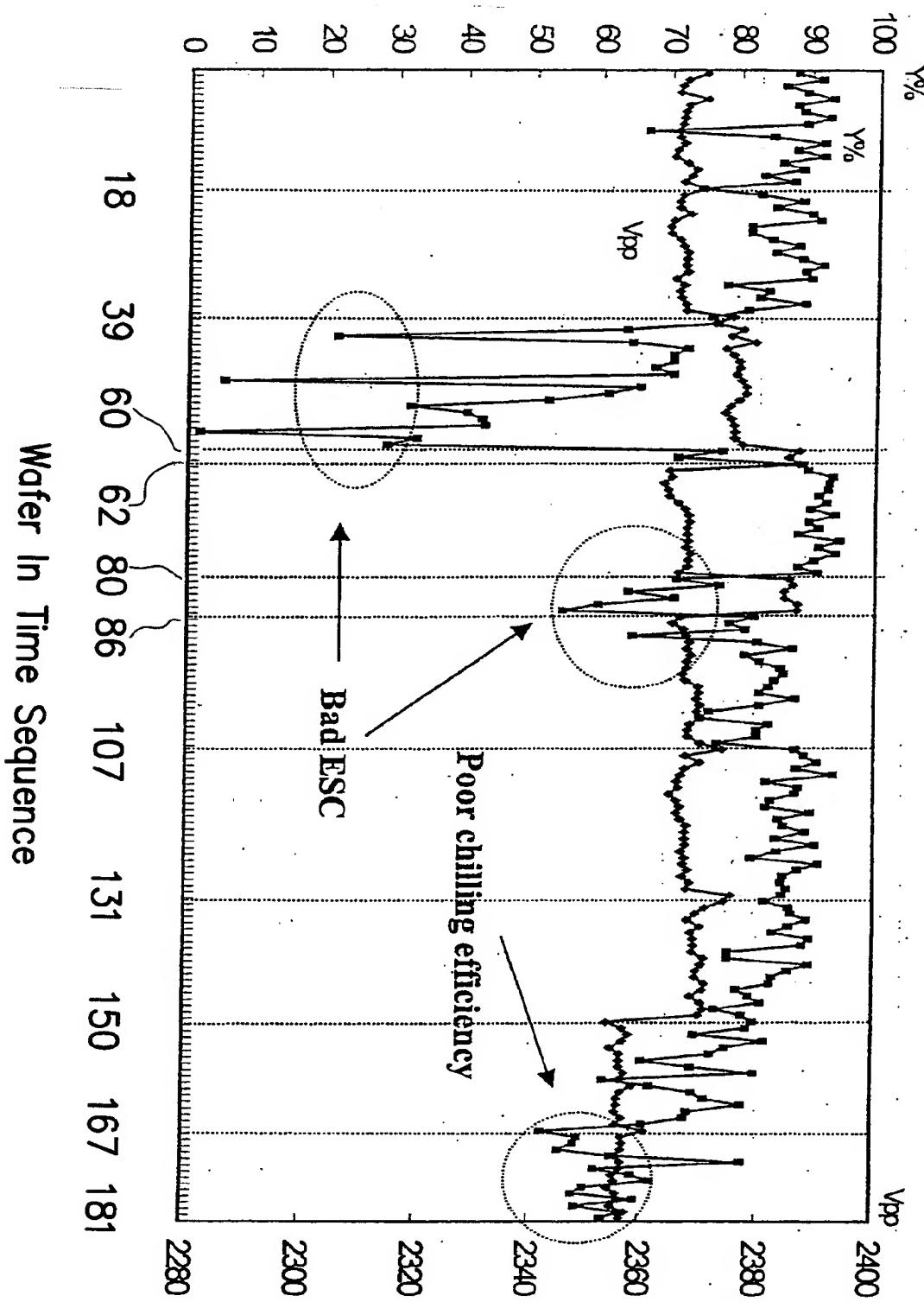


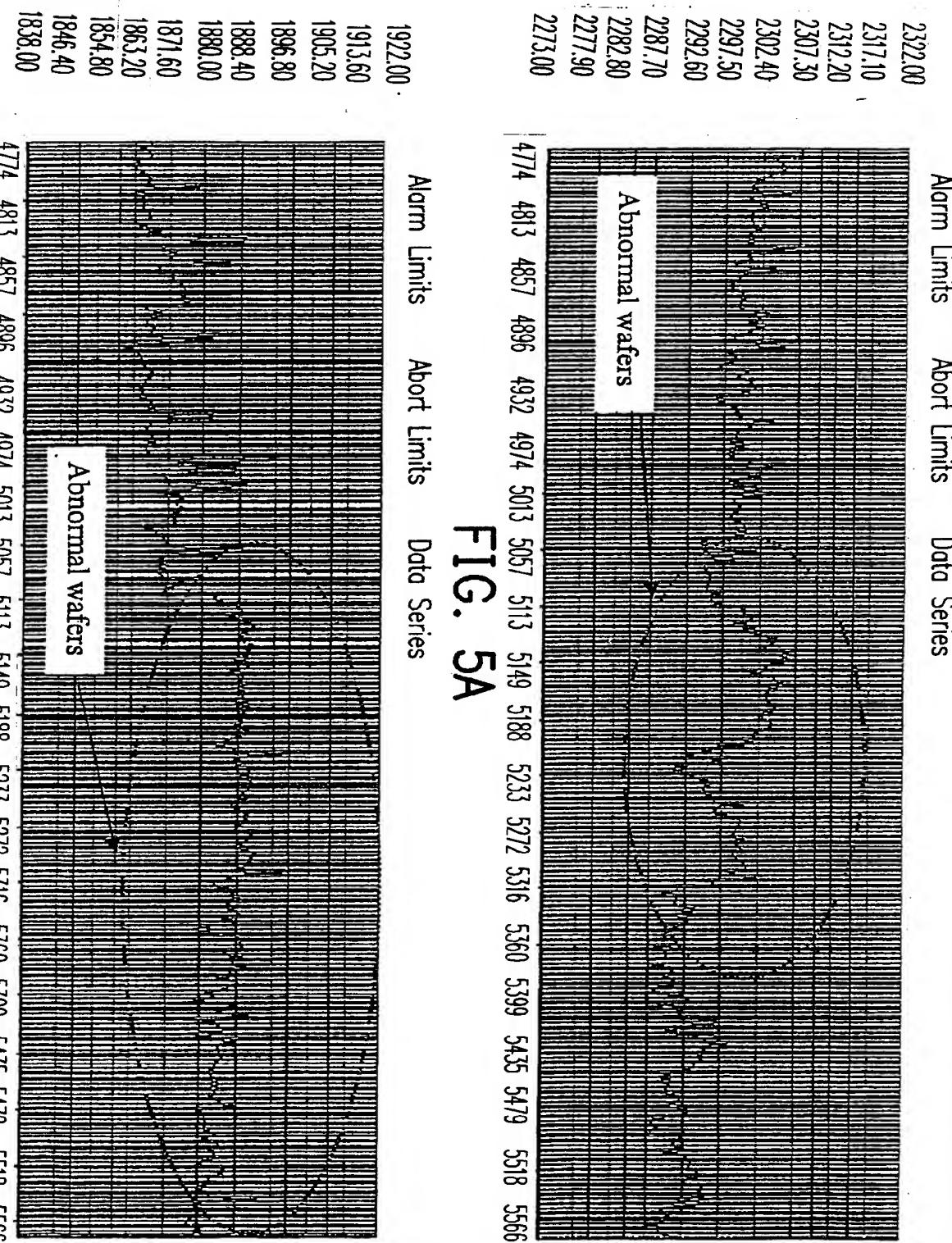
FIG. 2B



Wafer Sorted With  $Y\%$

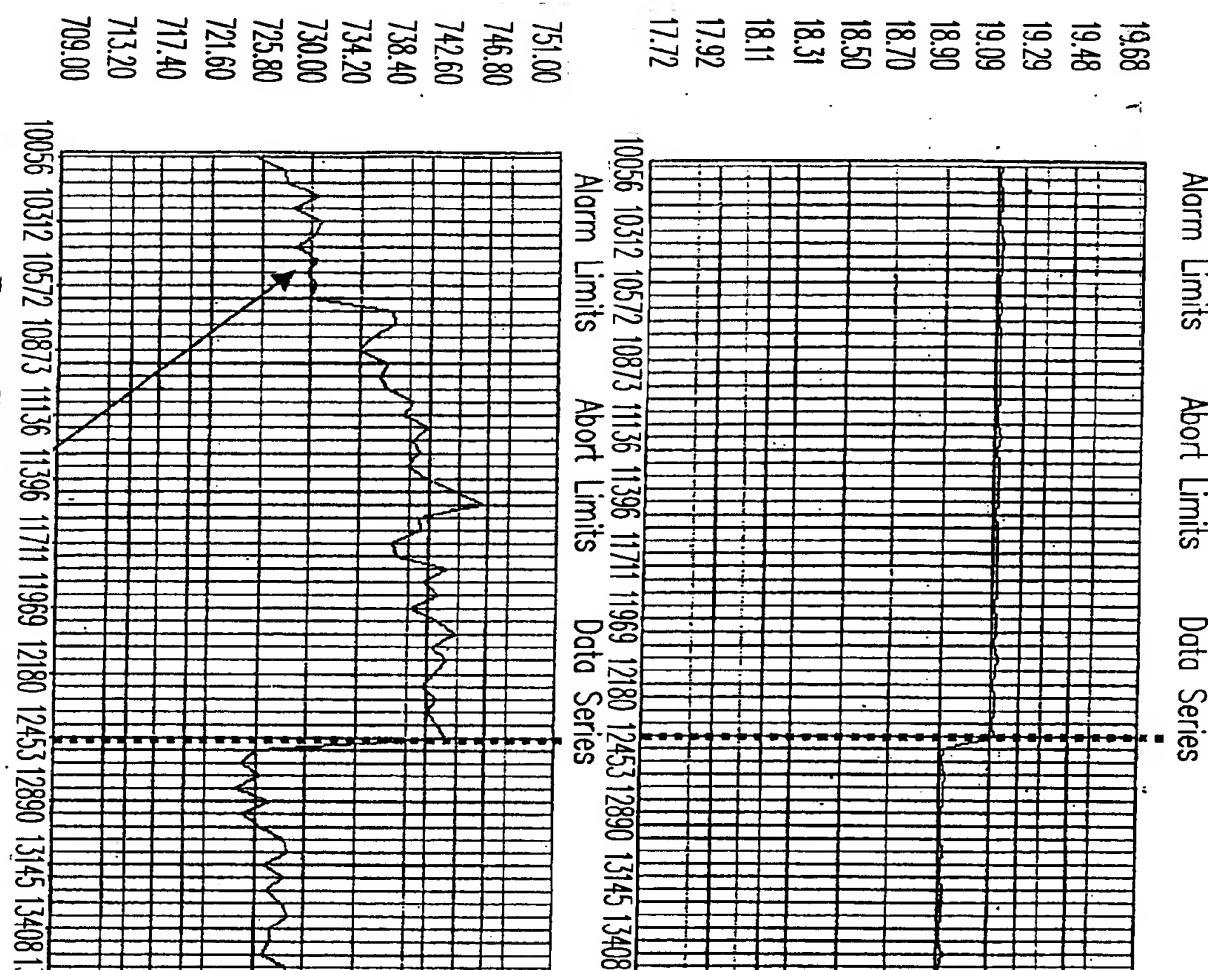
FIG. 3





**FIG. 5A**

**FIG. 5B**



**FIG. 6**

Bottom Temperature too high

$V_{pp}$

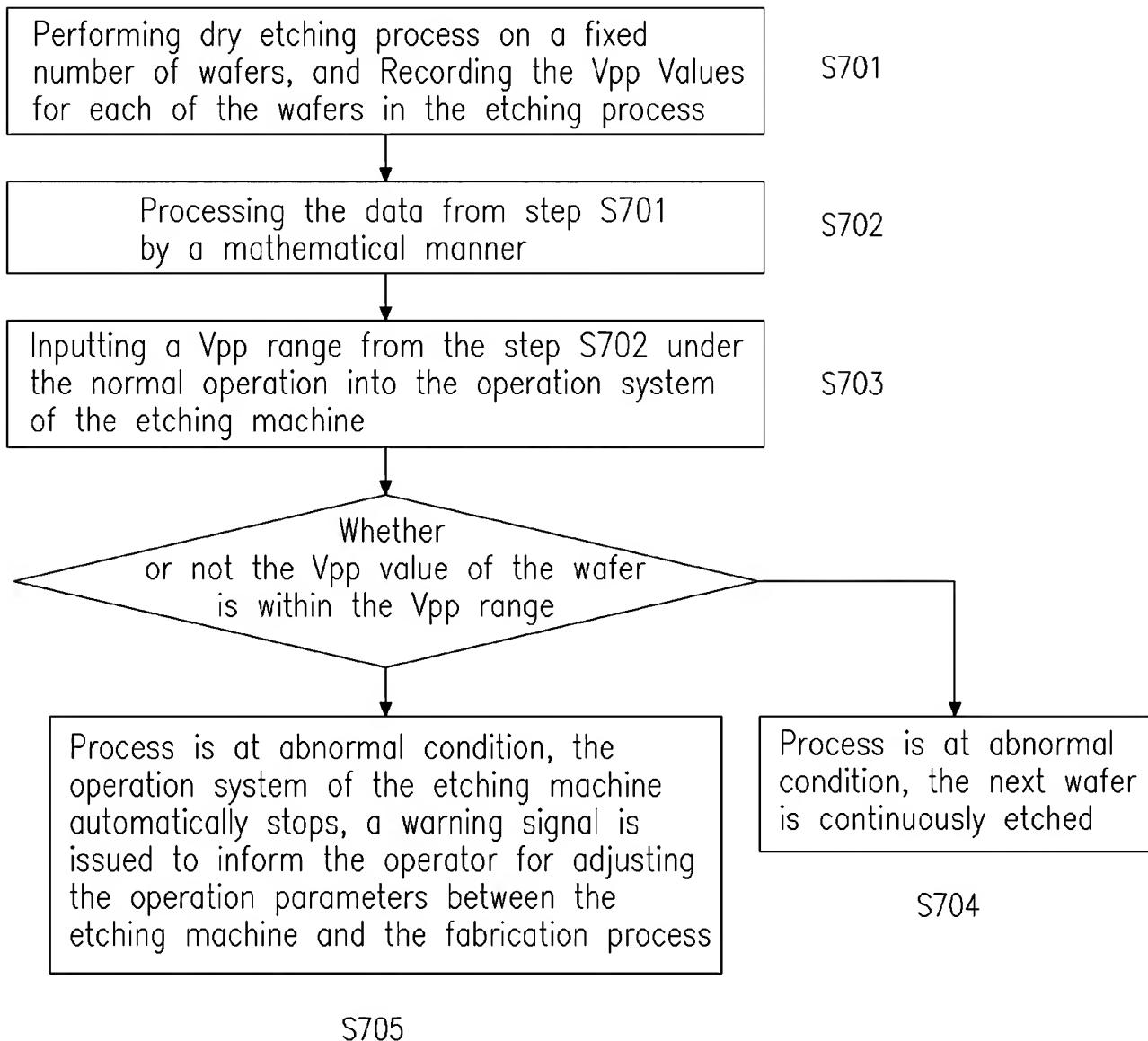


FIG. 7